## **AMENDMENT**

## In the claims:

Please amend claim 5, 21 and 22.

Please cancel claims 4 and 19.

- 1. (Cancelled)
- 2. (Previously Presented) The method of claim 21, wherein the first portion and the second portion are the same portion.
  - 3. (Cancelled)
  - 4. (Cancelled)
- 5. (Amended) The method of claim 214 further including the substep of: reading the second portion of the active decoded video from the first video memory and storing the at least second portion of the active decoded video in a first video memory associated with the first VGA.
- 6. (Original) The method of claim 5, wherein the first video memory and second video memory are accessed by a direct memory access (DMA) controller associated with the first VGA.
- 7. (Original) The method of claim 5, wherein the first video memory and second video memory are accessed by a direct memory access (DMA) controller on the second VGA.
- 8. (Previously Presented) The method of claim 21, wherein the first VGA is a primary VGA, and the second VGA is a secondary VGA.

- 9. (Previously Presented) The method of claim 21, wherein the first VGA is a secondary VGA, and the second VGA is a primary VGA.
- 10. (Previously Presented) The method of claim 21, wherein the first VGA and the second VGA are part of a video wall such that the first frame of active video is displayed across multiple displays simultaneously.
- 11. (Previously Presented) The method of claim 21 further comprising the steps of: receiving at the second VGA a second frame of active video from a second video source; and rendering at least a portion of the second frame of video at the first VGA.
  - 12. (Cancelled)
- 13. (Previously Presented) The method of claim 21 further comprising the step of storing the window location in a preference file.
  - 14. (Cancelled)
  - 15. (Cancelled)
  - 16. (Cancelled)
- 17. (Previously Presented) The method of claim 22, wherein the video decoder is for decoding a compressed video signal.
- 18. (Previously Presented) The method of claim 22, wherein the method further comprises the video source sending the first frame of data over a bus local to the first VGA.
  - 19. (Cancelled)

- 20. (Cancelled)
- 21. (Amended) A method of displaying active video on a computer system, the method comprising the steps of:
  - receiving at a first video graphics adapter (VGA) a first frame of active video from a video source;
  - rendering at least a first portion of the first frame of video at the first VGA in response to a first control signal, wherein the first control signal is a signal specifying a window location for displaying the active video;
  - storing the at least a first portion of the active video in a video memory associated with the first VGA; and
  - rendering at least a second portion of the first frame of video at a second VGA in response to a second control signal and storing the at least second portion of the active decoded video in a first memory associated with the first VGA.
- 22. (Previously Presented) A method of displaying active video on a computer system, the method comprising the steps of:
  - receiving at a first video graphics adapter (VGA) a first frame of active video from a video source, wherein video source is at least one of the following: a video decoder and a television signal;
  - storing the first frame of active video in a video memory associated with the first VGA; and
  - displaying at least a first portion of the first frame of video at a second VGA in response to a second control signal.